

Name: Dr. Gopal Agarwal

Designation: Assistant Prof. (ECE) (Grade – II, Level – 10)

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Professional Experience: Research – 1 year, Industrial – 1 year, Academics – 7 years and continuing

- Assistant Professor (Grade - II) at IIIT-Surat from 8th June, 2021 - to Present.
- Assistant Professor (on Contract) at IIIT-Surat from 1st Sept, 2020 to 7th June, 2021.
- Teaching Assistant at IIIT-Surat from 18th Feb, 2019 to 30th June, 2020.
- An additional cumulative teaching experience of 3 years, 16 days at a) SVNIT, Surat + b) SCET, Surat + c) GIDC-DEC, Abrama as Adhoc Assistant Professor during 18th Sep, 2015 to 15th Feb, 2019.
- Design Engineer at Sankalp Semiconductor Pvt. Ltd. {IIT-KGP Centre} from 12th April, 2010 to 30th April, 2011

Education:

- Ph.D. (E&C) - C U Shah University, Surendranagar, Gujarat
- M.Tech. (ICT) - DAIICT, Gandhinagar, Gujarat
- BE (E&C) - Sarvajanic College of Engineering and Technology, Surat, Gujarat

Research Interest: Embedded Systems, Analog VLSI Design, Autonomous Robot Navigation

Awards and Prizes: 1) Cleared UGC-NET December, 2019 and qualified for Assistant Professor with 99.89 percentile score 2) Qualified GATE exam for a total of 13 times so far with over 90 percentile score in every appearance

Research Output:

1. International Journals (Peer reviewed) – 1 (Submitted for Publication)
2. National Journals (Peer reviewed) – 1
https://www.iupindia.in/1910/Electrical%20and%20Electronics%20Engineering/Low_Voltage.asp
3. Books - 0
4. Book Chapter – 2
https://doi.org/10.1007/978-981-15-4851-2_35
https://doi.org/10.1007/978-981-15-7078-0_62
5. Others - 2
<https://ieeexplore.ieee.org/document/8064847>

Patents: - My work (<https://ieeexplore.ieee.org/document/8064847>) has been cited by US Patent No. 10101758 <https://patents.google.com/patent/US10101758>

Research Projects: