

<b>B.Tech. I Semester – 2</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>CS 201: DATA STRUCTURES AND ALGORITHMS</b>	<b>3</b>	<b>0</b>	<b>2</b>	<b>4</b>

<b>1. INTRODUCTION TO DATA STRUCTURE</b>	<b>04 Hrs</b>
Basic Terminology, Internal representation of Primitive Data structure: Integers, Floating point numbers, Packed decimal, Characters, Structures, Unions, and Pointers.	
<b>2. BASIC DATA TYPES</b>	
Arrays: Definition, Memory organization, Operations on Arrays: Traversing, Insertion, Deletion, Updating, Resizing	<b>03 Hrs</b>
Stacks: Basic operations, Stack, Dstack and applications	<b>02 Hrs</b>
Queues : Operations of queues, Circular Queue, Priority Queue, Dequeue, Application of queues	<b>03 Hrs</b>
Linked list : Singly linked lists and memory representation, Operations of Link list (Traversing, Searching, Insertion, Deletion, inversion, concatenation, copying and comparison, allocation and deallocation), Doubly linked list and operations, Circular Link list, Multilevel link list	<b>04 Hrs</b>
<b>3. TREES</b>	<b>09 Hrs</b>
Introduction, Binary Trees and their representation, Operations on Binary trees: Creation, transformation of trees into binary trees, traversal, Searching, Insertion and Deletion. Type of trees: Complete Binary trees, Extended binary trees, General trees, AVL trees, Threaded trees, B trees Application: Arithmetic expression evaluation, infix-prefix-postfix notation conversion.	
<b>4. GRAPH</b>	<b>08 Hrs</b>
Formal Introduction, types of graph, Representation of graphs: Sequential, List structure, Adjacency list, multilinked representation, Search in directed and undirected graphs, BFS, DFS, Transversal Connected Component and Spanning trees, Shortest path and Transitive Closure, Activity Networks, Topological Sort and Critical Paths.	
<b>5. ILLUSTRATED ALGORITHMS</b>	<b>09 Hrs</b>
Sorting (Bubble, Selection, Quick, Radix, Bucket sort, Heap sort), Dictionaries, hashing, analysis of collision resolution techniques, Searching(Linear Search, Binary Search), Character String and different string operations	
<b>Tutorials will be based on the coverage of the above topics separately</b>	<b>14 Hrs</b>
<b>Total: 42 Hours + 14 Hours = 56 Hours</b>	

<b>Recommended Books</b>
<ol style="list-style-type: none"> <li>1. "An Introduction to Data Structures with Applications", Trembley &amp; Sorenson, 2/E, TMH, 1991</li> <li>2. "Data Structures using C and C++", Tanenbaum &amp; Augenstein, 2/E, PHI, 2007</li> <li>3. "Fundamentals of Data Structures", Horowitz and Sahani, Galgotia Publications, reprint 2004.</li> <li>4. "Introduction to Algorithms", T. H. Cormen, C. E. Leiserson, R. L. Rivest, 2/E, PHI, 2001</li> <li>5. "Data Structures and Program Design in C", Robert L.Kruse, C.L.Tondo and Brence Leung, Pearson Education, 2/E, 2001</li> </ol>

<b>B.TECH. I Semester-2</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>EC 202: Electronic Devices and Circuits</b>	<b>3</b>	<b>0</b>	<b>2</b>	<b>4</b>

<b>Prerequisite</b>
Signals, Frequency Spectrum of Signals, Circuit Models for Amplifiers Intrinsic Semiconductors, Doped Semiconductors, Current Flow in Semiconductors

<b>Diodes</b>	<b>10 Hours</b>
The pn Junction, The pn Junction with an Applied Voltage, Capacitive Effects in the pn Junction The Ideal Diode, Terminal Characteristics of Junction Diodes, Modeling the Diode Forward Characteristic, Operation in the Reverse Breakdown Region—Zener Diodes, Rectifier Circuits, Limiting and Clamping Circuits, Special Diode Types	
<b>MOS Field-Effect Transistors &amp; Bipolar Junction Transistors</b>	<b>12 Hours</b>
<u>MOSFETs</u> Device Structure and Physical Operation, Current—Voltage Characteristics, MOSFET Circuits at DC, The Body Effect and Other Topics <u>BJTs</u> Device Structure and Physical Operation, Current—Voltage Characteristics, BJT Circuits at DC, Transistor Breakdown and Temperature Effects	
<b>Transistor Amplifiers</b>	<b>10 Hours</b>
Basic Principles, Small - Signal Operation and Models, Basic Configurations, Biasing, Discrete - Circuit Amplifiers	
<b>Building Blocks of Integrated Circuit Amplifiers, Differential Amplifiers</b>	<b>10 Hours</b>
IC Design Philosophy, IC Biasing—Current Sources, Current Mirrors, & Current-Steering Circuits, The Basic Gain Cell, The Common - Gate and Common - Base Amplifiers The MOS Differential Pair, The BJT Differential Pair, Common - Mode Rejection, DC Offset	
<b>Total Contact Time: 42 Hours</b>	

<b>Recommended Books</b>
<u>Text-Book</u> 1. A. S. Sedra and K. C. Smith, "Microelectronic Circuits", Oxford University Press, 7th Edition. <u>Reference-Books</u> 2. Boylestad Robert L. and Nashlesky Louis, "Electronics Device & Circuits and Theory", PHI, 10th Edition, 2009. 3. Millman Jacob, Halkias Christos C. and Parikh C., "Integrated Electronics", McGraw-Hill, 2nd Edition, 2009. 4. Schilling Donald L. and Belove E., "Electronics Circuits - Discrete and Integrated", McGraw-Hill, 3rd Edition, 1989, Reprint 2008. 5. "Electronic Principles", Malvin Albert & David J. Bates, Tata McGraw Hill, 7th edition, 2007. 6. "Pulse, Digital and Switching Waveforms", Millman J., Taub H. and Mothiki Suryaprakash, McGraw-Hill, 2nd Ed., 2007.

<b>B.Tech. I Semester – 2</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>AE 203: ENGINEERING MECHANICS</b>	<b>3</b>	<b>0</b>	<b>2</b>	<b>4</b>

<b>1. INTRODUCTION FORCES/EQUILIBRIUM OF RIGID BODY</b>	<b>06 Hrs</b>
Scalar and vectors, system of forces, resultant force, Statics of particles. Free-body diagrams. Equilibrium of particle in two dimensions, Resultants of three or more concurrent forces, Resolution of a force into components. Rectangular components of a force. Resultants by rectangular components, Concurrent force system in space: Resolution of a force into rectangular components in space, Coplanar Non- Concurrent Force Systems, Moments about Points and Axes. Equilibrium, Non-coplanar Non-concurrent Forces.	
<b>2. CENTROID MOMENT OF INERTIA</b>	<b>06 Hrs</b>
Distributed forces: Centroid and centre of gravity. Determination of centroid of lines and areas using integral technique, Determination of centroid of composite wires and areas, Centroid of volumes. Theorems of Pappus-Guldinus and its applications, Second moment of areas. Definition of moment of inertia. Determination of moment of areas by integration, Parallel axis theorem for Moment of Inertia. MI of composite areas.. Concept of Mass moment of inertia of bodies.	
<b>3. TRUSS</b>	<b>06 Hrs</b>
Types of structures in Engineering. Trusses and beams: definition, stability and determinateness, Determination of reactions at supports for planar trusses. Basic assumption for analysis of trusses. Procedures for analysis of trusses, Analysis of plane trusses by method of joints. Concept of zero force members. Analysis of plane trusses by method of sections.	
<b>4. BEAMS AND CABLES</b>	<b>10 Hrs</b>
Beams- Definitions, types of beams, types of loading, types of supports. Determination of reactions for simply supported and overhanging beams. Relation between distributed load, Shear force and Bending moment Shear force and Bending moment in beams with diagrams Cables- Cables with Concentrated Loads	
<b>5. FRICTION</b>	<b>04 Hrs</b>
The Laws of Dry Friction. Coefficients of Friction, Angles of Friction, Analysis of systems involving dry frictions such as ladders spheres etc., Belt Friction, Analysis of flat and v-belt.	
<b>6. KINETICS OF PARTICLES</b>	<b>05 Hrs</b>
Force and acceleration. Newton's laws of motion. D'Alembert's principle, Dependent motion of particles. Analysis for dependent motion of particles, Impulse and Momentum: Concept, Definition, Principle of linear impulse and momentum, Conservation of linear momentum of a system of particles.	
<b>7. VIBRATIONS</b>	<b>05 Hrs</b>
Definitions, Equation of motion for single degree of freedom, Introduction to free and forced vibrations. Procedure for analysis of system involving free and forced vibrations, Example on free vibration, Example on forced vibration, Concept of earthquake induced waves and its effect on structures.	
<b>Total: 42 Hrs</b>	

<b>Recommended Books</b>
<ol style="list-style-type: none"> <li>1. "Vector mechanics for engineers: Statics and Dynamics", Beer F.P. and Johnston E.R. Tata McGraw-Hill, New Delhi.</li> <li>2. "Engineering Mechanics: Statics and Dynamics", Desai J.A. and Mistry B.B., Popular Prakashan, Surat.</li> <li>3. "Engineering Mechanics: Statics and Dynamics", Hibbeler R.C., Prentice Hall of India, New Delhi.</li> </ol>

4. "Engineering Mechanics: Statics and Dynamics", Meriam J.L., and Kraige L.G., John Wiley and sons, New York.
5. "Engineering Mechanics: Statics and Dynamics", Rajsekaran S., Vikas Publication, New Delhi.
6. "Applied Mechanics", Shah H. J. and Junarkar S. B., Charotar publication, Anand.
7. "Engineering Mechanics", Bhavikatti S. S. and Rajashekarappa K. G., Wiley 'Eastern Ltd.

<b>B.TECH. I Semester-2</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>EC 204: Digital Logic Design</b>	<b>3</b>	<b>0</b>	<b>2</b>	<b>4</b>

<b>Binary Systems, Boolean Algebra and Logic Gates, Simplification of Boolean Functions</b>	<b>12 Hours</b>
Digital Computers and Digital Systems, Binary Numbers, Number-Base Conversions, Octal and Hexadecimal Numbers, Complements, Binary Codes, Binary Storage and Registers, Binary Logic, Integrated Circuits	
Basic Definitions, Axiomatic Definition of Boolean Algebra, Basic Theorems and Properties of Boolean Algebra, Boolean Functions, Canonical and Standard Forms, Other Logic Operations, Digital Logic Gates, IC Digital Logic Families	
The Map Method, Two- and Three-Variable Maps, Four-Variable Map, Five- and Six-Variable Maps, Product of Sums Simplification, NAND and NOR Implementation, Other Two-Level Implementations, Don't-Care Conditions, The Tabulation Method, Determination of Prime-Implicants, Selection of Prime-Implicants	
<b>Combinational Logic Combinational-Logic With MSI and LSI</b>	<b>8 Hours</b>
Introduction, Design Procedure, Adders, Subtractors, Code Conversion, Analysis Procedure, Multilevel NAND Circuits, Multilevel NOR Circuits, Exclusive-or and Equivalence Functions. Introduction, Binary Parallel Adder, Decimal Adder, Magnitude Comparator, Decoders, Multiplexers, Read-Only Memory (ROM), Programmable Logic Array (PLA)	
<b>Sequential Logic, Registers, Counters, And the Memory Unit</b>	<b>14 Hours</b>
Introduction, Flip-Flops, Triggering of Flip-Flops, Analysis of Clocked Sequential Circuits, State Reduction and Assignment, Flip-Flop Excitation Tables, Design Procedure, Design of Counters, Design with State Equations	
Introduction, Registers, Shift Registers, Ripple Counters, Synchronous Counters, Timing Sequences, The Memory Unit, Examples of Random Access Memories	
<b>Register Transfer Logic</b>	<b>8 Hours</b>
Introduction, Interregister Transfer, Arithmetic, Logic, and Shift Micro-Operations, Conditional Control Statements, Fixed-Point Binary Data, Overflow, Arithmetic Shifts, Decimal Data, Floating-Point Data, Non-Numeric Data, Instruction Codes, Design of Simple Computer	
<b>Total Contact Time: 42 Hours</b>	

<b>Recommended Books</b>
<ol style="list-style-type: none"> <li>1. "Digital Logic and Computer Design", Mano Morris, 3rd Edition, Pearson Education, 2005.</li> <li>2. "Digital Electronics Practice Using Integrated Circuits", Jain and Anand, TMH, 2004</li> <li>3. "Logic and Computer Design Fundamentals", Kime Charles, Pearson Education, 2004.</li> <li>4. "Digital Circuits and Logic Design", Lee Samuel, PHI, 1998.</li> <li>5. "Digital Fundamentals", Floyd and Jain, Pearson Education, 2006.</li> </ol>

<b>B.Tech. I Semester – 2</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>AS 205: DISCRETE MATHEMATICS</b>	<b>3</b>	<b>1</b>	<b>0</b>	<b>4</b>

<b>1. GRAPH THEORY</b>	<b>08 Hrs</b>
Graphs, Definition & basic concepts of finite & infinite graph, Incidence & Degree, Isomorphism, Subgraph, Walk, Path & circuits, Operations on graphs, connected graph, Disconnected graph & components, Complete graph, Regular graph, Bipartite graph, Euler's graph, Hamiltonian paths & circuits, Weighted graphs, Applications, Directed & Undirected graphs, Connectivity of graphs.	
<b>2. TREES</b>	<b>06 Hrs</b>
Definition & properties of trees, Pendent vertices in a tree, Distance between two vertices Centre, Radius & diameter of a tree, Rooted & binary trees, Representation of Algebraic structure by Binary trees, Binary search trees, Spanning trees & fundamental circuits.	
<b>3. RELATION &amp; LATTICES</b>	<b>08 Hrs</b>
Definition & Basic properties, Graphs of relation, Matrices of relation, Equivalence relation, Equivalence classes, Partition, Partial ordered relation, Posets, Hasse diagram, Upper bounds, Lower bound, GLB & LUB of sets, Definition & properties of Lattice, Sub lattice, Distributive & modular lattices, complemented & Bounded Lattices, complete lattices & Boolean algebra	
<b>4. GROUP THEORY</b>	<b>08 Hrs</b>
Basic properties of Group, Groupoid, semigroup & monoid, Abelian group, Subgroup, Cosets, Normal subgroup, Lagrange's theorem, Cyclic group , Permutation group, Homomorphism & Isomorphism of groups, Basic properties, error correction & detection code.	
<b>5. MATHEMATICAL LOGIC &amp; PROGRAM VERIFICATION</b>	<b>12 Hrs</b>
Propositions, logical operators & propositional algebra, Predicates & quantifiers, Interaction of quantifiers with logical operators, Logical interference & proof techniques, Formal verification of computer programs (elements of Hoare logic).	
<b>Total: 42 Hrs</b>	

<b>Recommended Books</b>
<ol style="list-style-type: none"> <li>1. "Discrete Mathematics and Its Applications", Rosen K.H., McGraw Hill, 6th Ed., 2006.</li> <li>2. "Discrete Mathematical Structure", Kolman B., Busby R.C. &amp; Ross S., Prentice Hall of India Pvt. Ltd, 5thEd, 2003.</li> <li>3. "Discrete Mathematical Structure with Applications to Computer Science", Tremblay J. P. &amp; Manohar R., McGraw Hill, 1999.</li> <li>4. "Graph theory with applications to Engineering &amp; Computer Science", DeoNarsingh., Prentice Hall of India Pvt. Ltd., 2000.</li> <li>5. "Elements of Discrete Mathematics", Liu C.L., McGraw Hill, 2000.</li> </ol>

<b>B.TECH. I Semester-2</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>EC 206: Signals and Systems</b>	<b>3</b>	<b>0</b>	<b>2</b>	<b>4</b>

<b>1. Discrete-time signals and systems</b>	<b>10 Hrs</b>
An Introduction to Signals and Systems, Signal Processing, Classification of Signals, The Concept of Frequency in Continuous-Time and Discrete-Time Signals.	
Discrete-Time Signals, Discrete -Time Systems, Analysis of Discrete -Time Linear Time - Invariant Systems, Discrete -Time Systems Described by Difference Equations, Implementation of Discrete - Time Systems, Correlation of Discrete - Time Signals.	
<b>2. The Laplace Transform and Z-Transform</b>	<b>12 Hrs</b>
Introduction, The Laplace Transform, The Region of Convergence for Laplace Transform, The inverse Laplace Transform, Geometric Evaluation of the Fourier Transform from the Pole Zero plot. Properties of the Laplace Transform.	
The Z-Transform, Properties of the Z -Transform, Rational Z-Transforms, Inversion of the Z-Transform. The One-sided Z-Transform. Analysis of Linear Time-Invariant Systems in the Z-Domain.	
<b>3. Frequency analysis of signals and systems</b>	<b>10 Hrs</b>
Frequency Analysis of Continuous-Time Signals. Frequency Analysis of Discrete -Time Signals. Properties of the Fourier Transform for Discrete -Time Signals. Frequency-Domain Characteristics of Linear Time -Invariant Systems. Linear Time - Invariant Systems as Frequency -Selective Filters. Inverse Systems and Deconvolution.	
<b>4. The discrete Fourier transform: its properties and Applications</b>	<b>10 Hrs</b>
Frequency Domain Sampling: The Discrete Fourier Transform. Properties of the DFT. Linear Filtering Methods Based on the DFT. Frequency Analysis of Signals Using the DFT.	
<b>Total: 42 Hrs</b>	

<b>Recommended Books</b>
<ol style="list-style-type: none"> <li>"Digital Signal Processing: Principle, Algorithms, and Applications", Prokis John G., Pearson Educations, 3rd Ed.,2003.</li> <li>"Signal and Systems", Oppenheim Alan V., Wilsky Alan S. and Nawab Hamid S., Pearson Educations, 3rd Ed., 2006.</li> <li>"Linear Systems and Signals", Lathi B. P., Oxford University Press, 2nd Ed., 2007.</li> <li>"Introduction to Signal and Systems", Stuller John Alan, Thomson India Edition, 1st Ed., 2007.</li> <li>"Fundamental of Signals and Systems", Roberts M. J. and Govind Sharma, Tata McGraw-Hill, 2nd Ed., 2010.</li> </ol>